

## **Job Opportunity for a Senior Communications FPGA/ASIC Engineer**

Silvus Technologies, Inc.  
10990 Wilshire Blvd., #440,  
Los Angeles, CA – 90024

### **The Opportunity:**

Silvus Technologies, Inc. is seeking a Senior Communications FPGA/ASIC Hardware Engineer to participate in the hardware design and implementation for a set of world-class OFDM, and MIMO based products. Application space includes WLAN, WiMax, and emerging cellular systems.

The selected individual will actively participate in all aspects of the product development process. Specifically these responsibilities include: architecture definition, FPGA implementation of communication and signal processing algorithms.

This opportunity is well suited for an entrepreneurial technologist with an established track record seeking to join an early stage startup with defense and commercial contracts on hand.

### **Technical Responsibilities:**

- Hardware (FPGA) architecture definition for SISO and MIMO based OFDM systems such as those being proposed for the emerging 802.11n standard.
- Fixed point architecture definition of the communication system.
- FPGA implementation of communication/signal processing algorithms.

### **Technical Qualifications:**

- MSEE required, PhD in Electrical Engineering a plus.
- 4-6 years of relevant communications hardware development experience.
- Developed and introduced into the market place of FPGA based products
- Experience with VHDL/Verilog and FPGAs a must.
- Knowledge of back end FPGA synthesis and P&R tools
- Experience with MATLAB highly desirable.
- Knowledge of signal processing algorithms a definite plus.
- Knowledge of MIMO systems a definite plus.

### **Contact Information:**

Interested candidates should fax their resume to 310-479-2225 or email to [jobs@silvustechnologies.com](mailto:jobs@silvustechnologies.com).